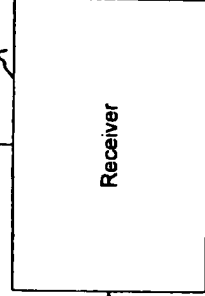
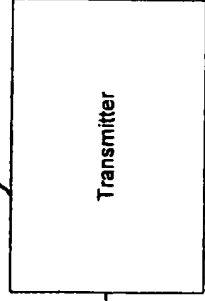
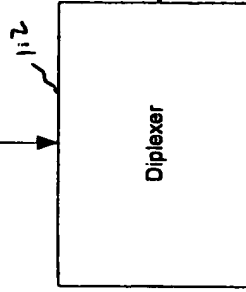
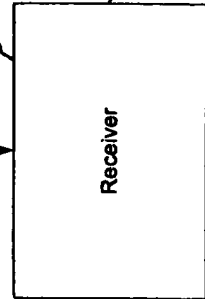
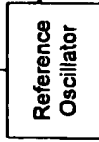
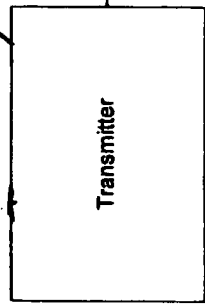


100

102

106

104



Sync Signal

Transmitter

Receiver

Duplexer

Duplexer

Reference
Oscillator

122

18

112

110

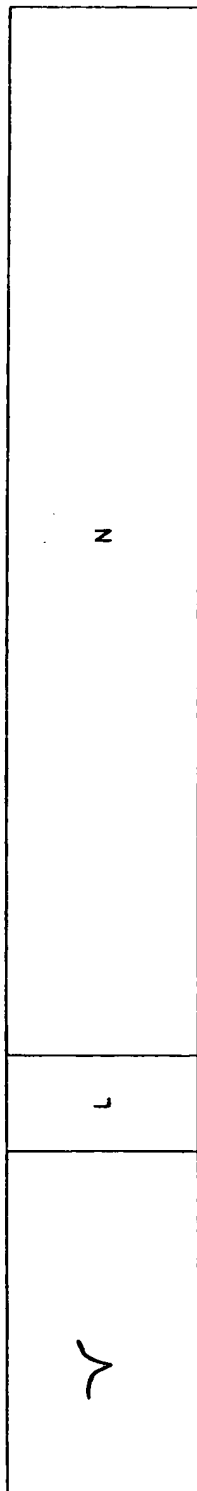
116

118

Fig. 1

2002

Fig. 2



300

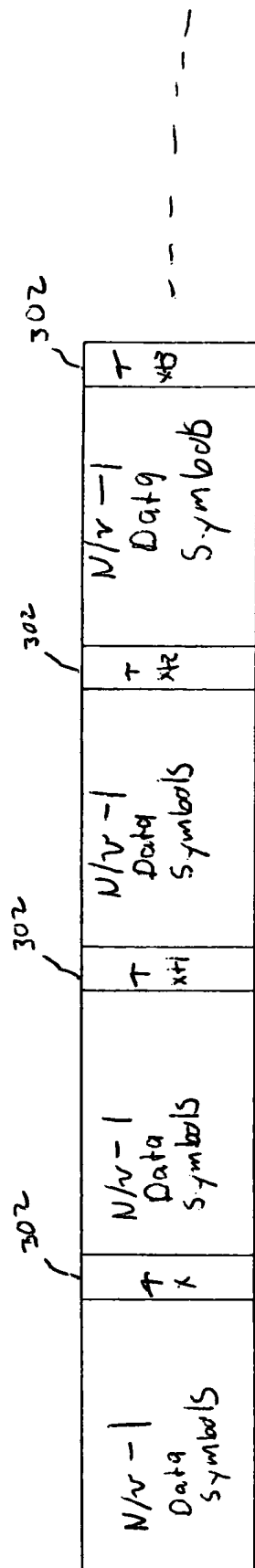


Fig. 3

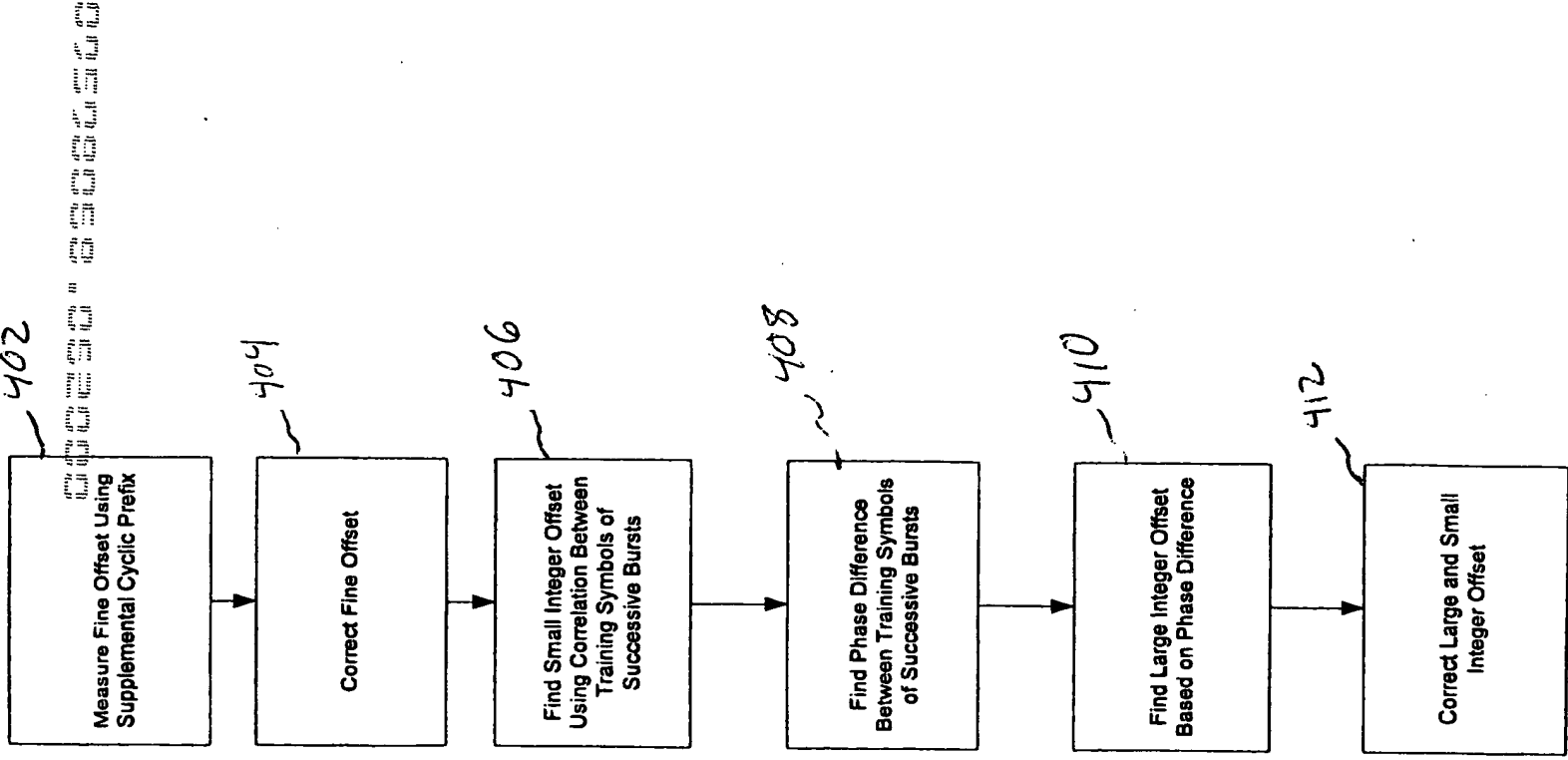


Fig. 4

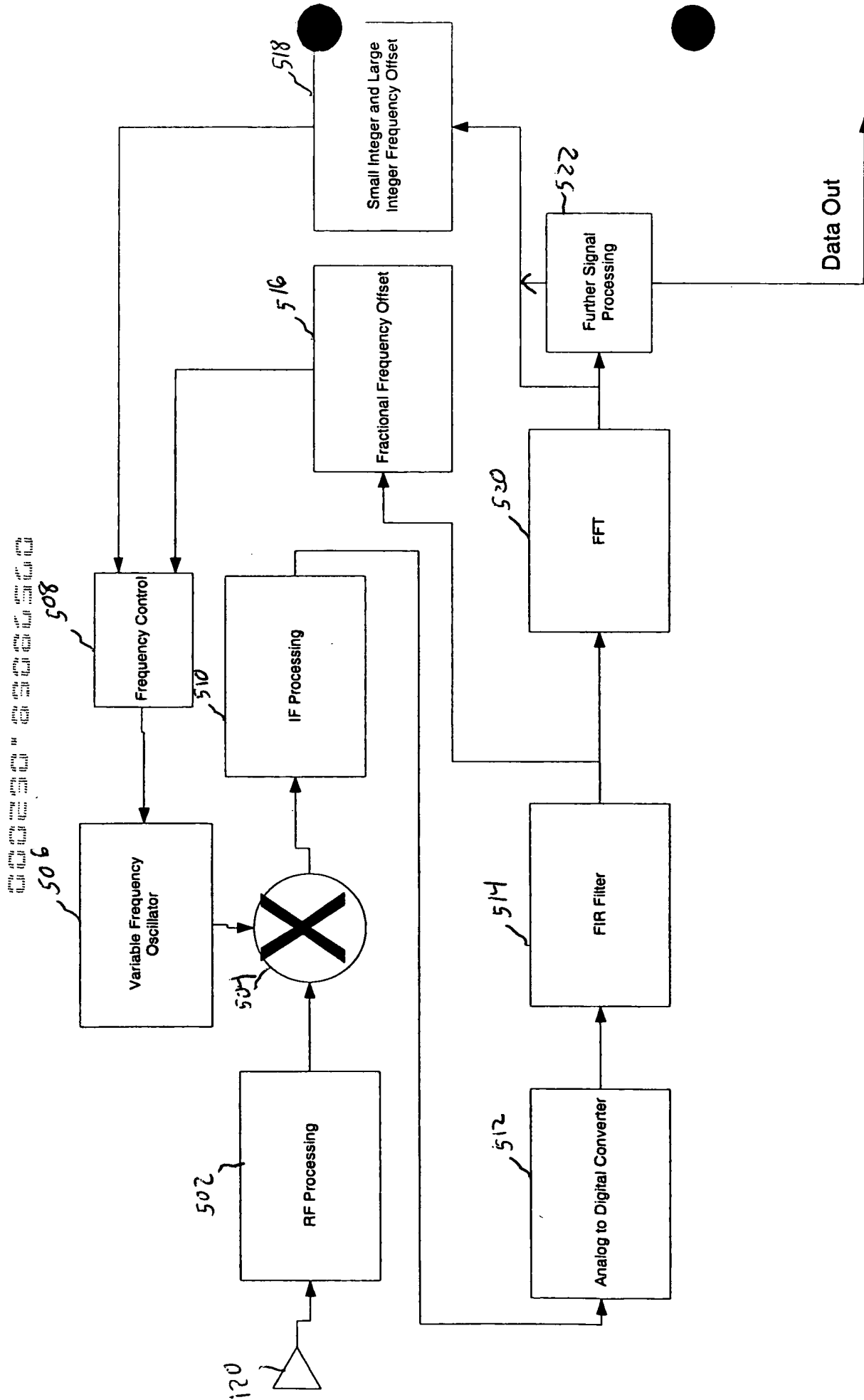


Fig. 5

FIG. 6 is a block diagram of a transmitter system 100, in accordance with an embodiment of the present invention.

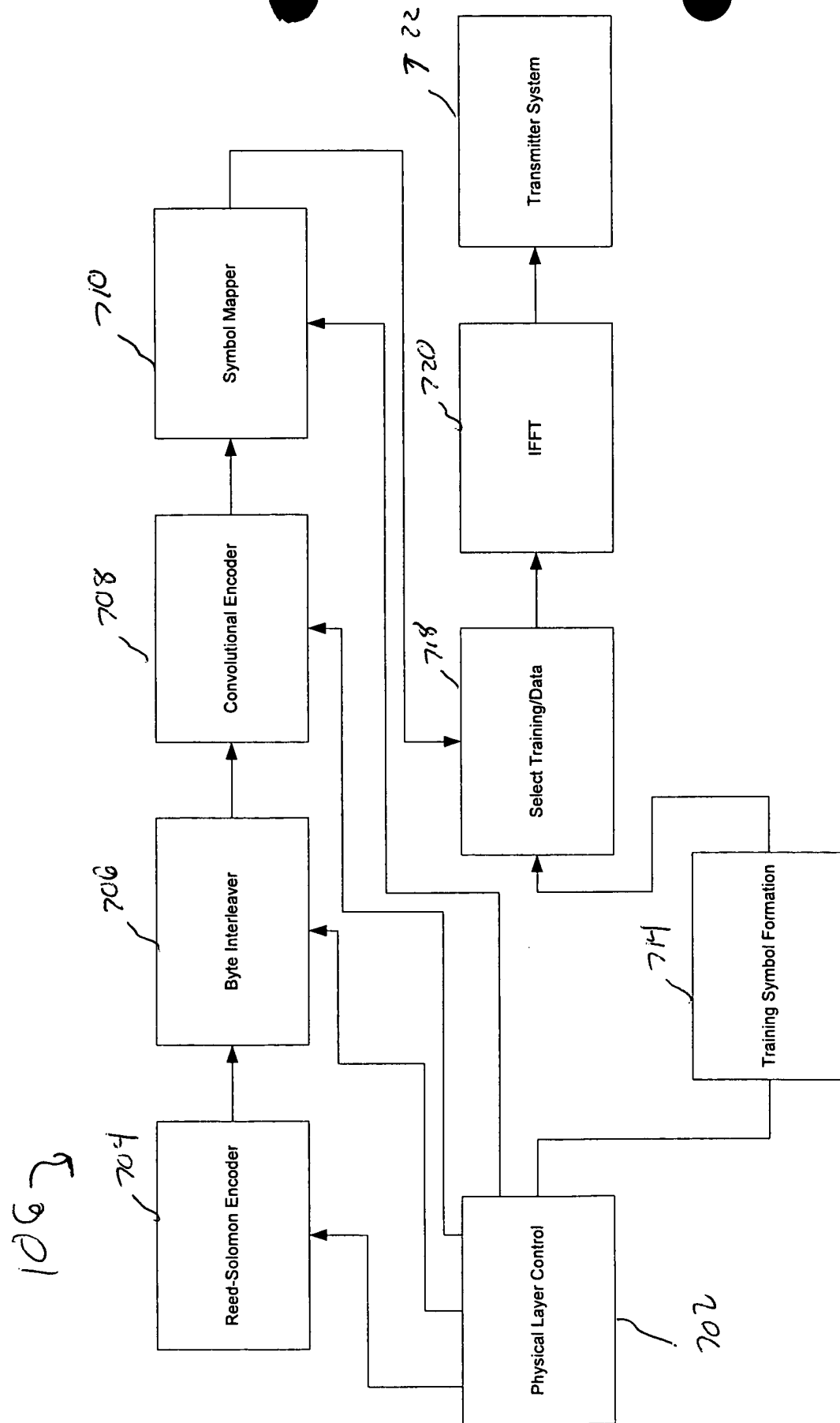


Fig. 6

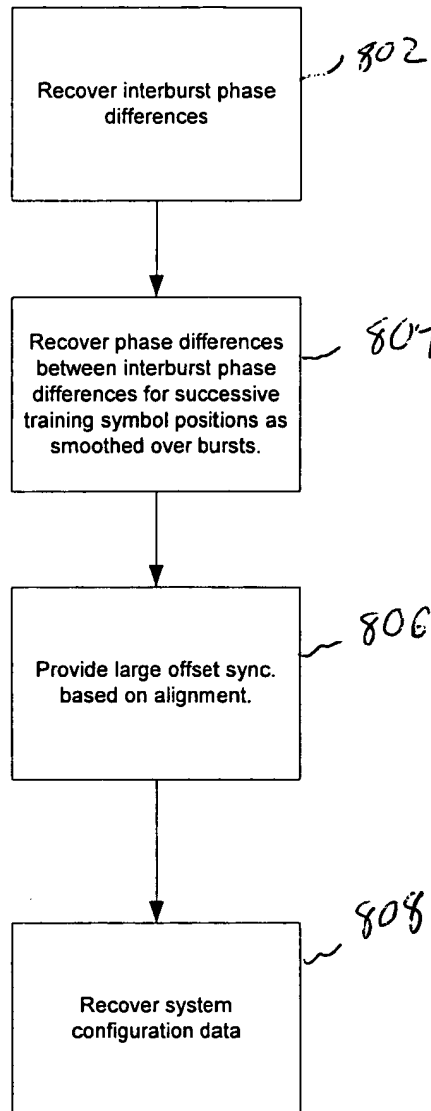


Fig. 7